

Global Learning Semesters

Course Syllabus

Course: CENG-281 Microprocessors Lab

Department: Engineering

Host Institution: Intercollege, Nicosia, Cyprus



Course Summary		
Course Code	Course Title	Recommended Credit Hours
CENG-281	Microprocessors Lab	1.5
Semester Offered	Contact Hours	Prerequisites
Spring	42	CENG-201 Digital Systems Lab. Experimentation on digital logic gates and circuits.
Department	Level of Course	Language of Instruction
Engineering	Lower Division	English

Course Description

The student is using the 16-bit BGC-8088 system micro-engineer, and the 32-bit microprocessor training unit (Lab-Volt) to perform a number of experiments on hardware architecture, bus operation, monitor commands and interfacing techniques.

Instructor

Mr. Andreas Serghiou

Course Aims and Objectives

To allow the students to conduct experiments that relate to microprocessor architecture programming, and interfacing techniques in realistic applications.

Teaching Methods

The course is delivered through laboratory experiments.

Course Teaching Hours

The course is 42 hours long and is delivered in 14 weeks (3 hours/week in a 3-hour session).

Evaluation and Grading

Lab Reports: 30%
Lab Performance: 30%
Final Exam: 40%

Readings and Resources

Required Textbooks

- User's Guide and Experiments, Introduction to BGC-8088 Micro engineer V3.0
- Lab-Volt 32-Bit Microprocessor Laboratory Manuals
- Barry B. Brey, Laboratory Manual to Accompany 8086/8088, 80186/80188, 80286, 80386, Pentium, and Pentium Pro Microprocessors, Prentice Hall, 1995

Recommended Reading

Barry B. Brey, The Intel Microprocessors 8086/8088, 80186/80188, 80286, 80386, 80486, Pentium, and Pentium Pro Processor, Sixth Edition, Prentice Hall, 2003