

# Global Learning Semesters

## Course Syllabus

Course: CENG-201A Digital Systems Lab

Department: Engineering

Host Institution: Intercollege, Nicosia, Cyprus



Course Summary		
Course Code	Course Title	Recommended Credit Hours
CENG-201A	Digital Systems Lab	1
Semester Offered	Contact Hours	Prerequisites
Please contact us	42-45	EENG-121
Department	Level of Course	Language of Instruction
Engineering	Lower Division	English

### Course Description

Selected experiments examining logic devices and circuits to accompany and complement the lecture course CENG-200

### Prerequisites

EENG-121

### Topic Areas

1. Binary numbers.
2. Basic logical operations.
3. Basic logic gates using discrete semiconductor components.
4. Logic probe and logic pulsar.
5. CMOS logic gates - interfacing.
6. Logic gate characteristics.
7. Combinational logic.
8. R-S and D type flip-flops.
9. The J-K flip-flop.
10. Binary addition.
11. Code converters.
12. Troubleshooting of digital systems.

### Readings and Resources

#### Recommended Readings

- Thomas L. Floyd, Digital Fundamentals, Eighth Edition, Prentice Hall, 2003 (ISBN: 0-13-046411-2).
- Jerry V. Cox, Digital Experiments: Emphasizing Troubleshooting, Merrill Publishing Company, 1986 (ISBN: 0-675-20518-2).